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Citation: *Appl. Phys. Lett.* **95**, 261106 (2009); doi: 10.1063/1.3277149

View online: <http://dx.doi.org/10.1063/1.3277149>

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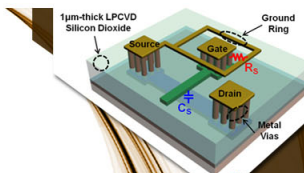
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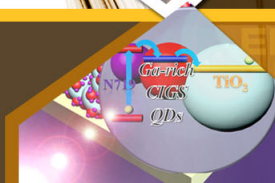
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Resistance to edge recombination in GaAs-based dots-in-a-well solar cells

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(Received 8 October 2009; accepted 3 December 2009; published online 30 December 2009)

Insensitivity to edge recombination is observed in GaAs-based InAs/InGaAs quantum dots-in-a-well (DWELL) solar cells by comparing its current-voltage (*IV*) plot to GaAs control samples. The edge recombination current component is extracted by analyzing devices of different areas and then compared to DWELL cells of comparable dimensions. The results demonstrate that GaAs-based solar cells incorporating a DWELL design are relatively insensitive to edge recombination by suppressing lateral diffusion of carriers in the intrinsic layer, and thus promising for applications that require small area devices such as concentration or flexible surfaces. © 2009 American Institute of Physics. [doi:10.1063/1.3277149]

Recent interest in using InAs quantum dots in the absorbing region of solar cells has focused primarily on the predicted increase in quantum efficiency due to the intermediate band effect or the ability to increase short circuit current density (J_{SC}) by extending the absorption edge further into the infrared range.^{1–4} However, relatively little attention has been paid to the unique carrier transport properties that are introduced into the device by the dots. It is well known that quantum dot (QD) structures efficiently confine carriers and thus inhibit the lateral spreading of current to the perimeter of a device where edge recombination can dominate.⁵ Consequently, QD solar cells are potentially insensitive to the edge or surface recombination current that would normally set a floor on the minimum cell area. This letter examines the phenomena by comparing the light/dark current behavior of “dots-in-a-well” (DWELL) cells^{6–8} and GaAs control cells of varying area. Combined with the known insensitivity of dots to temperature,⁹ the results are promising for concentration and flexible surface applications for which shrinking the size of the device and maintaining high charge collection efficiency are of paramount importance.¹⁰

A schematic diagram of the InAs/In_xGa_{1-x}As DWELL solar cell grown by molecular beam epitaxy (MBE) is illustrated in the inset of Fig. 1. The advantages of the DWELL approach have been discussed elsewhere.^{6–8} The six DWELL layers consist of InAs QDs that are sandwiched between InGaAs thin films which are placed within a 200 nm intrinsic GaAs region inside a *pin* diode. The sample is then topped with a 50 nm window layer, where the high quality MBE growth minimizes interface state density under the window layer. The total dot density is approximately $6 \times 10^{11} \text{ cm}^{-2}$. The GaAs control wafer consists of the same *pin* configuration but without the DWELL structure in the intrinsic region.

The control and DWELL samples were fabricated simultaneously to minimize process variation. The Ge/Au/Ni/Au emitter metallization creates the solar cell finger grid and is laid out in three different areal dimensions (5×5 , 3×3 , and $2 \times 2 \text{ mm}^2$). The bottom Ti/Pt/Au *p*-type contact is common for the solar cells on the sample. A 270-nm deep mesa, which reaches the intrinsic region, is dry-etched by inductively-

coupled plasma to separate neighboring solar cells with an isolation resistance of $\sim 10^5 \Omega$. The mesa is deliberately shallow which minimizes the device sidewall and potential recombination there. However, the shallow mesa exposes the device to lateral diffusion current that expands the perimeter of the cell around the *i*-region and nearby base. This process results in proportionally more of the current crowding at the edge of the device, even for the emitter. The DWELL structure inhibits this lateral diffusion, whereas the GaAs control cell does not.

For *IV* characterization, the cell is illuminated using an ABET Technologies 150 W Xe lamp. A filter is inserted between source and cell to simulate the AM1.5 G spectrum. The solar cell is connected to Hewlett Packard 4155 B parameter analyzer by a four-point probe approach to eliminate the series resistance introduced by the probes and the parameter analyzer. As shown in Fig. 1, the typical DWELL device exhibits higher J_{SC} while maintaining the same open circuit voltage (V_{OC}) for smaller areas. For the GaAs control cells, however, smaller size, which has a higher perimeter-to-area ratio, makes edge recombination current dominant in these devices, and, thus, severely impacts their V_{OC} and efficiency. Here V_{OC} of the $2 \times 2 \text{ mm}^2$ GaAs cell is 10% lower than the

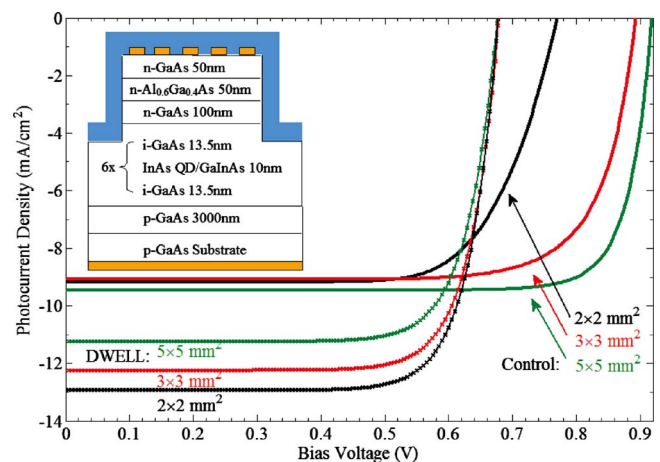


FIG. 1. (Color online) Photocurrent of DWELL and GaAs control cell of different sizes (2×2 , 3×3 , and $5 \times 5 \text{ mm}^2$) under AM 1.5 global illumination. The inserted picture is the schematic diagram of the DWELL solar cell with six-stacks of InAs QDs embedded in InGaAs quantum wells.

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TABLE I. Measured short circuit current densities (J_{sc}), open circuit voltages (V_{oc}), and efficiencies of the GaAs control cells and InAs DWELL solar cells under AM 1.5 G illumination.

Size	J_{sc} (mA/cm ²)		V_{oc} (V)		Efficiency (%)	
	Control	DWELL	Control	DWELL	Control	DWELL
5 × 5 mm ²	9.46	11.23	0.914	0.665	8.85	7.04
3 × 3 mm ²	9.08	12.23	0.89	0.67	7.61	7.79
2 × 2 mm ²	9.17	12.93	0.834	0.675	7.41	8.17

5 × 5 mm² one as shown in Fig. 1 and Table I.

To investigate the underlying physics of the V_{OC} degradation in the control samples, the dark IV is measured and the carrier recombination mechanism is analyzed. The conventional single diode model is¹¹

$$J_d = J_0 \left\{ \exp \left[\frac{V}{n(V)V_t} \right] - 1 \right\}, \quad (1)$$

where J_d and V are the measured dark current density and voltage bias, respectively. J_0 and $n(V)$ are the bias-dependent reverse saturation current density and local ideality factor, respectively. V_t is the thermal voltage of 0.0259 V and is constant during the experiment. When $J_0 \ll J_d$, and background radiation is negligible, the local ideality factor can be approximated as

$$n(V) = \frac{d(V/V_t)}{d[\ln(I)]}. \quad (2)$$

The ideality factors for both control and DWELL cells are measured as shown in Figs. 2(a) and 2(c). Substantial differences between the GaAs and DWELL cells include the shoulder in the GaAs cells' IV curve and resulting hump in the $n(V)$. Neither of these effects is observed for any area in the DWELL cells. The peak in the ideality factor is more significant as the area of the GaAs cell decreases, which suggests that edge recombination is significant. Another series of wafer growths and processing produced the same results. This strongly voltage-dependent ideality factor can be modeled by the pinning of the Fermi level to surface states at the device perimeter.^{12–15}

TABLE II. Parameters used in curve-fitting control samples using SRH statistics.

Descriptions		2 × 2 mm ²	3 × 3 mm ²	5 × 5 mm ²
n_i	Intrinsic carrier density (cm ⁻³)		1.8 × 10 ⁶	
dn	Injected carrier density (cm ⁻³)		$n_i \exp(V/2V_t)$	
n_b	Ideality factor for bulk		1.31	
J_{b0}	Reverse saturation current density for bulk (mA/cm ²)		1.2 × 10 ⁻¹⁰	
S_{p0}	Holes' surface recombination rate (cm/s)	0.8 × 10 ⁷	1.0 × 10 ⁷	1.0 × 10 ⁷
S_{n0}	Electrons' surface recombination rate (cm/s)	7 × 10 ⁷	3 × 10 ⁷	2 × 10 ⁷
p_{s0}	Surface holes density (cm ⁻³)	6 × 10 ¹³	1 × 10 ¹³	3 × 10 ¹³
n_{s0}	Surface electrons density (cm ⁻³)		n_i^2/p_{s0}	
R_s	Series resistance (Ω)	2.2	1.0	0.6
$d \times P/A$	Exposed edge surface to top area ratio	1.0 × 10 ⁻⁵	5.5 × 10 ⁻⁶	2.5 × 10 ⁻⁶
p_s	Bias-dependent surface hole density (cm ⁻³)		$p_{s0} + dn$	
n_s	Bias-dependent surface electron density (cm ⁻³)		$n_{s0} + dn$	
n_i, p_i	Electron, hole doping density (cm ⁻³)		negligible	

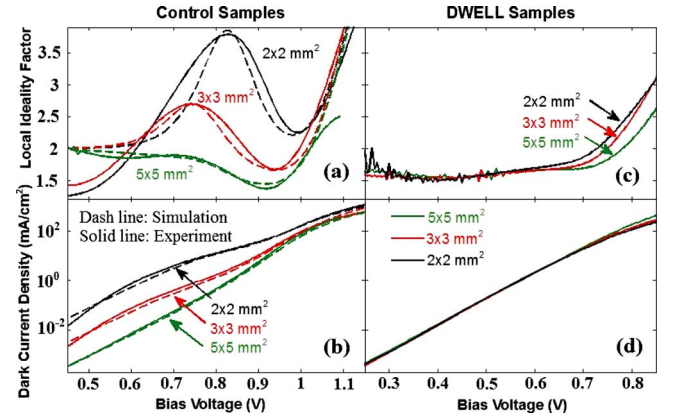


FIG. 2. (Color online) A comparison of the dark behavior of GaAs control and DWELL cells for the same dimensions (2 × 2, 3 × 3, and 5 × 5 mm²). (a) Measured and simulated local ideality factor for the control cells, (b) Measured and simulated semi-logarithmic dark current density for the control cells, (c) Tested local ideality factor, and (d) Dark current density for DWELL cells. The simulation is based on Eq. (3), where the parameters extracted by curve fitting are illustrated in Table II.

After taking the edge recombination and series resistance into account, the total dark current expression can be adjusted from Eqs. (1) to (3)

$$J_d = J_b + J_p, \quad (3a)$$

$$J_b = J_{b0} \exp \left(\frac{V - J_d \times A \times R_s}{n_b V_t} \right), \quad (3b)$$

$$J_p = q \frac{n_s p_s - n_i^2}{(n_s + n_i)/S_{p0} + (p_s + p_i)/S_{n0}} \times d \times \frac{P}{A}, \quad (3c)$$

where J_b and J_p are contributions from bulk and perimeter of the cell, respectively. The fitting parameters for this model used to simulate the data in Figs. 2(a) and 2(b) are described in Table II. J_b follows the conventional diode equation and J_p is modeled using Shockley–Read–Hall (SRH) statistics as expressed in Eq. (3c).¹⁶ It is assumed that the ideality factor (n_b) and reverse saturation current density (J_{b0}) of the bulk diode are constants over the bias range where SRH recombination dominates. The surface carrier density (p_{s0}, n_{s0}) influences the peak location of the hump in the ideality factor in Fig. 2(a), and the surface recombination

rate (S_{p0}, S_{n0}) determines the shape of the hump. At high bias (>0.8 V), the series resistance (R_s) dominates the trend. Based on these features, the model is adjusted to fit the tested ideality factor and dark current density [Figs. 2(a) and 2(b)].

Good agreement is achieved between the model and the data for Figs. 2(a) and 2(b). It is found that the edge recombination current is proportional to the perimeter of the cell, while the bulk current scales with the cell area. Therefore, as predicted by the simulation and confirmed experimentally, the smaller cells, which have a comparatively larger P/A ratio, are more susceptible to the edge recombination phenomenon. Any minor disagreement between the experiment and model can be explained by our assumption that there is uniform edge recombination current across the device perimeter and that n_b and J_{b0} are constants. The edge recombination component has been simplified to a one-dimensional model with constant etched depth and surface states over the exposed perimeter. In reality, however, the recombination current is most intense near the contact fingers and decreases with distance away from the metal edges. This was verified by two-dimensional electroluminescence of the device and a SILVACO ATLAS simulation.

Although the DWELL and GaAs control cell were processed in the same run, the humps in the ideality factor disappear completely in all of the DWELL cells as demonstrated in Fig. 2(c). Similar to previously published observations,⁵ the DWELL structure is effective at blocking lateral current flow to the device perimeter where surface recombination can occur. Although the dark current densities of the DWELL cells are generally higher, the values seen in the controls would have been observable in the DWELL devices. Thus, the overlapping *IV* curves shown in Fig. 2(d) for different size DWELL devices further supports the idea that the dots play an effective role in suppressing lateral diffusion of carriers.

In summary, compared to GaAs pin diode cells that experimentally display degradation of the dark current and ideality factor as the device perimeter/area ratio is increased, solar cells with an InAs/InGaAs DWELL structure positioned in the intrinsic region do not exhibit this problem. The strong peaking of the ideality factor in the GaAs control cells

has been theoretically explained by a model that includes bulk and edge recombination effects. Since a hump in the ideality factor of the DWELL cell is completely absent, it is concluded that the DWELL structure limits lateral current movement and subsequent edge recombination. The DWELL devices should be especially useful for concentrated and flexible solar applications for which small area devices are highly desirable.

This work was supported in part by Dr. Kitt Reinhardt of the Air Force Office of Scientific Research under Grant No. FA9550-06-1-0407 and the Micro-Autonomous Systems Technology (MAST) program administered by the U.S. Army Research Laboratory.

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